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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Application Serial No.	10/618,471
Confirmation No	5311
Filing Date	July 11, 2003
Inventor	
Assignee	
Group Art Unit	·
Examiner	
Attorney's Docket No	Ml22-2331
Customer No.	
Title: Semiconductor Processing Methods of Forming Integrated Circuitry	

## RESPONSE TO OCTOBER 29, 2004 PRELIMINARY AMENDMENT TO ACCOMPANY RCE FILING

To:

Mail Stop RCE

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

From:

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This Preliminary Amendment is filed in response to the October 29, 2004 Office Action, and accompanies Request for Continued Examination. Please enter the following amendments prior to examining the above-identified application. <u>Underlines</u> indicate insertions and <u>strikeouts</u> indicate deletions.

## <u>AMENDMENT</u>

Amendments to the Specification begin on Page 2 of this paper.

Amendments to the Claims begin on Page 3 of this paper.

Remarks begin on Page 7 of this paper.

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